Failure Modes, Effects and Diagnostic Analysis

Project:
One Series SAFETY TRANSMITTER

Company:
United Electric Controls
Watertown, MA
USA

Contract Number: Q17/01-143
Report No.: UE 12/10-073 R001
Version V3, Revision R3, April 20, 2017
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Management Summary

This report summarizes the results of the hardware assessment in the form of a Failure Modes, Effects, and Diagnostic Analysis (FMEDA) of the One Series SAFETY TRANSMITTER, hardware and software revision per Section 2.5.1. A Failure Modes, Effects, and Diagnostic Analysis is one of the steps to be taken to achieve functional safety certification of a device per IEC 61508. From the FMEDA, failure rates are determined. The FMEDA that is described in this report concerns only the hardware of the SAFETY TRANSMITTER. For full functional safety certification purposes all requirements of IEC 61508 must be considered.

Product Classification

The One Series SAFETY TRANSMITTER is classified as both a Safety Pressure Transmitter and a Safety Temperature Transmitter.

The One Series SAFETY TRANSMITTER is a smart device which senses temperature or pressure and provides a 4-20mA and/or solid state relay outputs. It contains self-diagnostics and is programmed to send its output to a specified failure state upon internal detection of a failure. The SAFETY TRANSMITTER also provides an “I Am Working” output as well as a switch status output which echoes the state of the relay output.

Table 1 gives an overview of the different versions that were considered in the FMEDA of the SAFETY TRANSMITTER.

Table 1 Version Overview

Pressure Current IAW	Pressure input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is monitored.
Temperature Current IAW	Temperature input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is monitored
Pressure Current no IAW	Pressure input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is not monitored.
Temperature Current no IAW	Temperature input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is not monitored
Pressure AC Relay IAW	Pressure input; the de-energize-to-trip AC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Temperature AC Relay IAW	Temperature input; the de-energize-to-trip AC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Pressure DC Relay IAW	Pressure input; the de-energize-to-trip DC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Temperature DC Relay IAW	Temperature input; the de-energize-to-trip DC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Pressure Status IAW	Pressure input; the de-energize-to-trip status output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Temperature Status IAW	Temperature input; the de-energize-to-trip status output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.

The SAFETY TRANSMITTER is classified as a Type B[1] element according to IEC 61508, having a hardware fault tolerance of 0.

The failure rate data used for this analysis meets the *exida* criteria for Route 2_H (see Section 5.4). Therefore, the SAFETY TRANSMITTER meets the hardware architectural constraints for up to SIL 2 at HFT=0 (or SIL 3 @ HFT=1) when the listed failure rates are used.

Based on the assumptions listed in 4.3, the failure rates for the SAFETY TRANSMITTER are listed in section 4.4.

These failure rates are valid for the useful lifetime of the product, see Appendix A.

[1] Type B element: "Complex" element (using micro controllers or programmable logic); for details see 7.4.4.1.3 of IEC 61508-2, ed2, 2010.

The failure rates listed in this report are based on over 250 billion unit operating hours of process industry field failure data. The failure rate predictions reflect realistic failures and include site specific failures due to human events for the specified Site Safety Index (SSI), see section 4.2.2.

A user of the SAFETY TRANSMITTER can utilize these failure rates in a probabilistic model of a safety instrumented function (SIF) to determine suitability in part for safety instrumented system (SIS) usage in a particular safety integrity level (SIL).

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1 Purpose and Scope

This document shall describe the results of the hardware assessment in the form of the Failure Modes, Effects and Diagnostic Analysis carried out on the SAFETY TRANSMITTER. From this, failure rates for each failure mode/category, useful life, and proof test coverage are determined.

The information in this report can be used to evaluate whether an element meets the average Probability of Failure on Demand (PFD_{AVG}) requirements and if applicable, the architectural constraints / minimum hardware fault tolerance requirements per IEC 61508 / IEC 61511.

A FMEDA is part of the effort needed to achieve full certification per IEC 61508 or other relevant functional safety standard.

2 Project Management

2.1 *exida*

exida is one of the world's leading accredited Certification Bodies and knowledge companies, specializing in automation system safety cybersecurity, and availability with over 400 years of cumulative experience in functional safety. Founded by several of the world's top reliability and safety experts from assessment organizations and manufacturers, *exida* is a global company with offices around the world. *exida* offers training, coaching, project oriented system consulting services, safety lifecycle engineering tools, detailed product assurance, cyber-security and functional safety certification, and a collection of on-line safety and reliability resources. *exida* maintains a comprehensive failure rate and failure mode database on process equipment based on 250 billion unit operating hours of field failure data.

2.2 Roles of the parties involved

United Electric Controls Manufacturer of the SAFETY TRANSMITTER

exida Performed the hardware assessment

United Electric Controls contracted *exida* in January 2017 with the hardware assessment of the above-mentioned device.

2.3 Standards and literature used

The services delivered by *exida* were performed based on the following standards / literature.

[N1]	IEC 61508-2: ed2, 2010	Functional Safety of Electrical/Electronic/Programmable Electronic Safety-Related Systems
[N2]	Electrical Component Reliability Handbook, 4th Edition, 2017	*exida* LLC, Electrical Component Reliability Handbook, Fourth Edition, 2017
[N3]	Mechanical Component Reliability Handbook, 4th Edition, 2017	*exida* LLC, Electrical & Mechanical Component Reliability Handbook, Fourth Edition, 2017
[N4]	Goble, W.M. 2010	Control Systems Safety Evaluation and Reliability, 3rd edition, ISA, ISBN 97B-1-934394-80-9. Reference on FMEDA methods
[N5]	IEC 60654-1:1993-02, second edition	Industrial-process measurement and control equipment – Operating conditions – Part 1: Climatic condition
[N6]	O'Brien, C. & Bredemeyer, L., 2009	*exida* LLC., Final Elements & the IEC 61508 and IEC Functional Safety Standards, 2009, ISBN 978-1-9934977-01-9

[N7]	Scaling the Three Barriers, Recorded Web Seminar, June 2013,	Scaling the Three Barriers, Recorded Web Seminar, June 2013, http://www.exida.com/Webinars/Recordings/SIF-Verification-Scaling-the-Three-Barriers
[N8]	Meeting Architecture Constraints in SIF Design, Recorded Web Seminar, March 2013	http://www.exida.com/Webinars/Recordings/Meeting-Architecture-Constraints-in-SIF-Design
[N9]	Random versus Systematic – Issues and Solutions, September 2016	Goble, W.M., Bukowski, J.V., and Stewart, L.L., Random versus Systematic – Issues and Solutions, *exida* White Paper, PA: Sellersville, www.exida.com/resources/whitepapers, September 2016.
[N10]	Assessing Safety Culture via the Site Safety Index™, April 2016	Bukowski, J.V. and Chastain-Knight, D., Assessing Safety Culture via the Site Safety Index™, Proceedings of the AIChE 12th Global Congress on Process Safety, GCPS2016, TX: Houston, April 2016.
[N11]	Quantifying the Impacts of Human Factors on Functional Safety, April 2016	Bukowski, J.V. and Stewart, L.L., Quantifying the Impacts of Human Factors on Functional Safety, Proceedings of the 12th Global Congress on Process Safety, AIChE 2016 Spring Meeting, NY: New York, April 2016.
[N12]	Criteria for the Application of IEC 61508:2010 Route 2H, December 2016	Criteria for the Application of IEC 61508:2010 Route 2H, *exida* White Paper, PA: Sellersville, www.exida.com, December 2016.
[N13]	Using a Failure Modes, Effects and Diagnostic Analysis (FMEDA) to Measure Diagnostic Coverage in Programmable Electronic Systems, November 1999	Goble, W.M. and Brombacher, A.C., Using a Failure Modes, Effects and Diagnostic Analysis (FMEDA) to Measure Diagnostic Coverage in Programmable Electronic Systems, Reliability Engineering and System Safety, Vol. 66, No. 2, November 1999.
[N14]	FMEDA – Accurate Product Failure Metrics, June 2015	Grebe, J. and Goble W.M., FMEDA – Accurate Product Failure Metrics, www.exida.com, June 2015.

2.4 *exida* tools used

[T1]	V7.1.18	*exida* FMEDA Tool

2.5 Reference documents

2.5.1 Documentation provided by United Electric Controls

[D1]	Doc # SR113028.D2.5, Rev B, 2012-12-26	System Architecture Description
[D2]	Doc # SR113028.D3.2, Rev A, 2013-06-17	Circuit Descriptions

[D3]	Doc # SR113028.D4.2, Rev E, 2016-08-15	Software Architecture Description
[D4]	Doc # 6247-691, Rev E, 2013-06-10	Schematic Drawing, Main Board
[D5]	Doc # 6247-692, Rev E, 2013-07-01	Schematic Drawing, AC Relay Board
[D6]	Doc # 6247-710, Rev A, 2017-02-27	Schematic Drawing, DC Relay Board
[D7]	SR#113028.D3.8, 2013-11-12	Fault Injection Test Report
[D8]	Doc # IM_ONE ST-05, Rev DRAFT, April 2017	One Series SAFETY TRANSMITTER Installation and Operation Manual
[D9]	Doc # SR160005.D3.2, Rev A, 2016-08-22	Circuit Descriptions, DC Output Solid State Relays

2.5.2 Documentation generated by *exida*

[R1]	UE1S Main Board Pressure Current IAW 2014-04-04.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Pressure Input, Current Output, IAW monitored
[R2]	UE1S Main Board Pressure Current No IAW 2014-04-04.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Pressure Input, Current Output, IAW not monitored
[R3]	UE1S Main Board Pressure Discrete IAW 2014-04-03.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Pressure Input Relay Output, IAW monitored
[R4]	UE1S Main Board Pressure Status IAW 2014-04-03.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Pressure Input Status Output, IAW monitored
[R5]	UE1S Main Board Temperature Current IAW 2014-04-03.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Temperature Input Current Output, IAW monitored
[R6]	UE1S Main Board Temperature Current No IAW 2014-04-02.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Temperature Input Current Output, IAW not monitored
[R7]	UE1S Main Board Temperature Discrete IAW 2014-04-03.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Temperature Input, Relay Output, IAW monitored
[R8]	UE1S Main Board Temperature Status IAW 2014-04-04.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Main Board, Temperature Input, Status Output, IAW monitored

[R9]	UE1S Relay Board Current IAW 2014-04-02.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Relay Board, Current Output, IAW monitored
[R10]	UE1S Relay Board Current No IAW 2014-04-02.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Relay Board, Current Output, IAW not monitored
[R11]	UE1S Relay Board Discrete IAW 2014-02-02.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Relay Board, Relay Output
[R12]	UE1S Relay Board Status IAW 2014-02-02.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER Relay Board, Status Output
[R13]	UE1S Summary 2017-04-10.xls	Failure Modes, Effects, and Diagnostic Analysis - Summary –SAFETY TRANSMITTER
[R14]	UE1S DC Relay Board Current IAW 2017-04-10.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER DC Relay Board, Current Output, IAW monitored
[R15]	UE1S DC Relay Board Current No IAW 2017-04-10.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER DC Relay Board, Current Output, IAW not monitored
[R16]	UE1S DC Relay Board Discrete IAW 2017-04-10.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER DC Relay Board, Relay Output
[R17]	UE1S DC Relay Board Status IAW 2017-04-10.efm	Failure Modes, Effects, and Diagnostic Analysis – SAFETY TRANSMITTER DC Relay Board, Status Output

3 Product Description

Product Classification

The One Series SAFETY TRANSMITTER is classified as both a Safety Pressure Transmitter and a Safety Temperature Transmitter.

The One Series SAFETY TRANSMITTER is a smart device which senses temperature or pressure and provides a 4-20mA and/or solid state relay outputs. It contains self-diagnostics and is programmed to send its output to a specified failure state upon internal detection of a failure. The SAFETY TRANSMITTER provides an “I Am Working” output as well as a switch status output which echoes the state of the relay output.

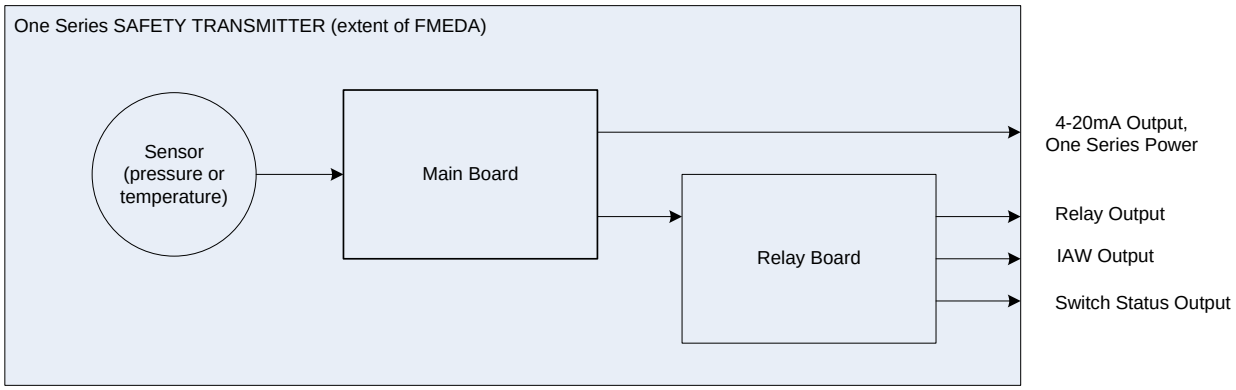

Figure 1 SAFETY TRANSMITTER, Parts included in the FMEDA

Table 2 gives an overview of the different versions that were considered in the FMEDA of the SAFETY TRANSMITTER.

Table 2 Version Overview

Pressure Current IAW	Pressure input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is monitored.
Temperature Current IAW	Temperature input; the externally energized 4-20mA current loop supplies the safety variable to a logic solver. The IAW output is monitored
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Temperature AC Relay IAW	Temperature input; the de-energize-to-trip AC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Pressure DC Relay IAW	Pressure input; the de-energize-to-trip DC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Temperature DC Relay IAW	Temperature input; the de-energize-to-trip DC relay output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Pressure Status IAW	Pressure input; the de-energize-to-trip status output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.
Temperature Status IAW	Temperature input; the de-energize-to-trip status output provides the safety variable to a logic solver or directly to the final element. The IAW output is monitored.

The SAFETY TRANSMITTER is classified as a Type B[2] element according to IEC 61508, having a hardware fault tolerance of 0.

[2] Type B element: "Complex" element (using micro controllers or programmable logic); for details see 7.4.4.1.3 of IEC 61508-2, ed2, 2010.

4 Failure Modes, Effects, and Diagnostic Analysis

The Failure Modes, Effects, and Diagnostic Analysis was performed based on the documentation in section 2.5.1 and is documented in [R1] to [R17].

When the effect of a certain failure mode could not be analyzed theoretically, the failure modes were introduced on component level and the effects of these failure modes were examined on system level, see Fault Injection Test Report [D7].

4.1 Failure categories description

In order to judge the failure behavior of the SAFETY TRANSMITTER, the following definitions for the failure of the device were considered.

Fail-Safe State	Failure that deviates the process signal or the actual output by more than 3% of span, drifts toward the user defined threshold (Trip Point) and that leaves the output within the active scale.
Fail Safe	Failure that causes the device to go to the defined fail-safe state without a demand from the process.
Fail Detected	Failure that causes the output signal to go to the predefined alarm state (3.7 mA).
Fail Dangerous	Failure that deviates the process signal or the actual output by more than 3% of span, drifts away from the user defined threshold (Trip Point) and that leaves the output within the active scale.
Fail Dangerous Undetected	Failure that is dangerous and that is not being diagnosed by automatic diagnostics.
Fail Dangerous Detected	Failure that is dangerous but is detected by automatic diagnostics.
Fail High	Failure that causes the output signal to go to the over-range or high alarm output current (> 21 mA).
Fail Low	Failure that causes the output signal to go to the under-range or low alarm output current (< 3.8 mA).
No Effect	Failure of a component that is part of the safety function but that has no effect on the safety function.
Annunciation Detected	Failure that does not directly impact safety but does impact the ability to detect a future fault (such as a fault in a diagnostic circuit) and that is detected by internal diagnostics. A Fail Annunciation Detected failure leads to a false diagnostic alarm.
Annunciation Undetected	Failure that does not directly impact safety but does impact the ability to detect a future fault (such as a fault in a diagnostic circuit) and that is not detected by internal diagnostics.

The failure categories listed above expand on the categories listed in IEC 61508 in order to provide a complete set of data needed for design optimization.

Depending on the application, a Fail High or a Fail Low failure can either be safe or dangerous and may be detected or undetected depending on the programming of the logic solver. Consequently, during a Safety Integrity Level (SIL) verification assessment the Fail High and Fail Low failure categories need to be classified as safe or dangerous, detected or undetected.

The Annunciation failures are provided for those who wish to do reliability modeling more detailed than required by IEC61508. It is assumed that the probability model will correctly account for the Annunciation failures.

4.2 Methodology – FMEDA, failure rates

4.2.1 FMEDA

A FMEDA (Failure Mode Effect and Diagnostic Analysis) is a failure rate prediction technique based on a study of design strength versus operational profile stress. It combines design FMEA techniques with extensions to identify automatic diagnostic techniques and the failure modes relevant to safety instrumented system design. It is a technique recommended to generate failure rates for each failure mode category [N13, N14].

4.2.2 Failure rates

The accuracy of any FMEDA analysis depends upon the component reliability data as input to the process. Component data from consumer, transportation, military or telephone applications could generate failure rate data unsuitable for the process industries. The component data used by *exida* in this FMEDA is from the Electrical and Mechanical Component Reliability Handbooks [N3] which were derived using over 250 billion unit operational hours of process industry field failure data from multiple sources and failure data formulas from international standards. The component failure rates are provided for each applicable operational profile and application, see Appendix C. The *exida* profile chosen for this FMEDA was 2, judged to be the best fit for the product and application information submitted by United Electric Controls. It is expected that the actual number of field failures will be less than the number predicted by these failure rates.

Early life failures (infant mortality) are not included in the failure rate prediction as it is assumed that some level of commission testing is done. End of life failures are not included in the failure rate prediction as useful life is specified.

The failure rates are predicted for a Site Safety Index of SSI=2 [N10, N11] as this level of operation is common in the process industries. Failure rate predictions for other SSI levels are included in the exSILentia® tool from *exida*.

The user of these numbers is responsible for determining the failure rate applicability to any particular environment. *exida* Environmental Profiles listing expected stress levels can be found in Appendix C. Some industrial plant sites have high levels of stress. Under those conditions the failure rate data is adjusted to a higher value to account for the specific conditions of the plant. *exida* has detailed models available to make customized failure rate predictions. Contact *exida* for more information.

Accurate plant specific data may be used to check validity of this failure rate data. If a user has data collected from a good proof test reporting system such as *exida* SILStat™ that indicates higher failure rates, the higher numbers shall be used.

4.3 Assumptions

The following assumptions have been made during the Failure Modes, Effects, and Diagnostic Analysis of the SAFETY TRANSMITTER.

- The worst case assumption of a series system is made. Therefore only a single component failure will fail the entire SAFETY TRANSMITTER and propagation of failures is not relevant.
- Failure rates are constant for the useful life period.
- Any product component that cannot influence the safety function (feedback immune) is excluded. All components that are part of the safety function including those needed for normal operation are included in the analysis.
- The stress levels are specified in the *exida* Profile used for the analysis are limited by the manufacturer's published ratings.
- Practical fault insertion tests have been used when applicable to demonstrate the correctness of the FMEDA results.
- The application program in the logic solver is constructed in such a way that Fail High and Fail Low failures are detected regardless of the effect, safe or dangerous, on the safety function.
- Materials are compatible with process conditions.
- The device is installed and operated per manufacturer's instructions.
- External power supply failure rates are not included.
- Worst-case internal fault detection time is 6 seconds.

4.4 Results

Using reliability data extracted from the *exida* Electrical and Mechanical Component Reliability Handbook the following failure rates resulted from the SAFETY TRANSMITTER FMEDA.

Table 3 Failure rates SAFETY TRANSMITTER Pressure Current IAW

Failure Category	Failure Rate (FIT)	
Fail Safe Undetected		76
Fail Dangerous Detected		3429
Fail Detected (detected by internal diagnostics)	3391	
Fail High (detected by logic solver)	16	
Fail Low (detected by logic solver)	22	
Fail Dangerous Undetected		42
No Effect		331
Annunciation Undetected		24

Table 4 Failure rates SAFETY TRANSMITTER Temperature Current IAW

Failure Category	Failure Rate (FIT)	
Fail Safe Undetected		76
Fail Dangerous Detected		3442
Fail Detected (detected by internal diagnostics)	3408	
Fail High (detected by logic solver)	15	
Fail Low (detected by logic solver)	19	
Fail Dangerous Undetected		42
No Effect		330
Annunciation Undetected		24

Table 5 Failure rates SAFETY TRANSMITTER Pressure Current no IAW

Failure Category	Failure Rate (FIT)	
Fail Safe Undetected		78
Fail Dangerous Detected		3400
Fail Detected (detected by internal diagnostics)	3363	
Fail High (detected by logic solver)	17	
Fail Low (detected by logic solver)	20	
Fail Dangerous Undetected		48
No Effect		331
Annunciation Undetected		53

Table 6 Failure rates SAFETY TRANSMITTER Temperature Current no IAW

Failure Category	Failure Rate (FIT)	
Fail Safe Undetected		76
Fail Dangerous Detected		3409
Fail Detected (detected by internal diagnostics)	3375	
Fail High (detected by logic solver)	15	
Fail Low (detected by logic solver)	19	
Fail Dangerous Undetected		45
No Effect		331
Annunciation Undetected		52

Table 7 Failure rates SAFETY TRANSMITTER Pressure AC Relay IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1711
Fail Safe Undetected	76
Fail Dangerous Detected	1700
Fail Dangerous Undetected	80
No Effect	297
Annunciation Detected	44
Annunciation Undetected	26

Table 8 Failure rates SAFETY TRANSMITTER Temperature AC Relay IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1711
Fail Safe Undetected	76
Fail Dangerous Detected	1719
Fail Dangerous Undetected	80
No Effect	297
Annunciation Detected	44
Annunciation Undetected	26

Table 9 Failure rates SAFETY TRANSMITTER Pressure DC Relay IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1708
Fail Safe Undetected	76
Fail Dangerous Detected	1702
Fail Dangerous Undetected	80
No Effect	315
Annunciation Detected	57
Annunciation Undetected	27

Table 10 Failure rates SAFETY TRANSMITTER Temperature DC Relay IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1708
Fail Safe Undetected	76
Fail Dangerous Detected	1721
Fail Dangerous Undetected	81
No Effect	315
Annunciation Detected	57
Annunciation Undetected	27

Table 11 Failure rates SAFETY TRANSMITTER Pressure Status IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1666
Fail Safe Undetected	106
Fail Dangerous Detected	1690
Fail Dangerous Undetected	46
No Effect	333
Annunciation Detected	28
Annunciation Undetected	25

Table 12 Failure rates SAFETY TRANSMITTER Temperature Status IAW

Failure Category	Failure Rate (FIT)
Fail Safe Detected	1668
Fail Safe Undetected	106
Fail Dangerous Detected	1710
Fail Dangerous Undetected	46
No Effect	335
Annunciation Detected	28
Annunciation Undetected	25

These failure rates are valid for the useful lifetime of the product, see Appendix A.

According to IEC 61508 the architectural constraints of an element must be determined. This can be done by following the 1_H approach according to 7.4.4.2 of IEC 61508 or the 2_H approach according to 7.4.4.3 of IEC 61508 (see Section 5.4).

The 1_H approach involves calculating the Safe Failure Fraction for the entire element.

The 2_H approach involves assessment of the reliability data for the entire element according to 7.4.4.3.3 of IEC 61508.

The failure rate data used for this analysis meets the *exida* criteria for Route 2_H. Therefore, the SAFETY TRANSMITTER meets the hardware architectural constraints for up to SIL 2 at HFT=0 (or SIL 3 @ HFT=1) when the listed failure rates are used.

Table 13 lists the failure rates for the SAFETY TRANSMITTER according to IEC 61508. This summary data table represents a reduced worst-case data set for the major product options.

Table 13 Failure rates according to IEC 61508 in FIT

Device	λ_{SD}	λ_{SU}[3]	λ_{DD}	λ_{DU}
Pressure or Temperature, Current Output with IAW	0	76	3429	42
Pressure or Temperature, Current Output with no IAW	0	78	3400	48
Pressure or Temperature, AC or DC Relay Output with IAW	1755	76	1700	80
Pressure or Temperature, Status Output with IAW	1694	106	1690	46

[3] It is important to realize that the No Effect failures are no longer included in the Safe Undetected failure category according to IEC 61508, ed2, 2010.

5 Using the FMEDA Results

The following section(s) describe how to apply the results of the FMEDA.

5.1 Impulse line clogging

The SAFETY TRANSMITTERcan be connected to the process using impulse lines; depending on the application, the analysis needs to account for clogging of the impulse lines. The SAFETY TRANSMITTER failure rates that are displayed in section 4.4 are failure rates that reflect the situation where the transmitter is used in clean service. Clean service indicates that failure rates due to clogging of the impulse line are not counted. For applications other than clean service, the user must estimate the failure rate for the clogged impulse line and add this failure rate to the SAFETY TRANSMITTER failure rates.

5.2 Temperature sensing devices

The SAFETY TRANSMITTER together with a temperature-sensing device becomes a temperature sensor assembly. Therefore, when using the results of this FMEDA in a SIL verification assessment, the failure rates and failure modes of the temperature sensing device must be considered. Typical failure rates for close-coupled thermocouples and RTDs are listed in Table 14.

Table 14 Typical failure rates close-coupled thermocouples and RTDs

Temperature Sensing Device	Failure rate (FIT)
Thermocouple low stress environment	100
Thermocouple high stress environment	2,000
4-wire RTD low stress environment	50
4-wire RTD high stress environment	1,000

5.2.1 SAFETY TRANSMITTER with thermocouple

The failure mode distributions for thermocouples vary in published literature but there is strong agreement that open circuit or “burn-out” failure is the dominant failure mode. While some estimates put this failure mode at 99%+, a more conservative failure rate distribution suitable for SIS applications is shown in Table 15 when close-coupled thermocouples are supplied with the SAFETY TRANSMITTER. The drift failure mode is primarily due to T/C aging. The SAFETY TRANSMITTER will detect a thermocouple burnout failure and drive the analog output to the specified failure state.

Table 15 Typical failure mode distributions for thermocouples

TC Failure Modes – Close-coupled device	Percentage
Open Circuit (Burn-out)	95%
Wire Short (Temperature measurement in error)	4%
Drift (Temperature measurement in error) (50% Safe; 50% Dangerous)	1%

A complete temperature sensor assembly consisting of SAFETY TRANSMITTER and a closely coupled thermocouple supplied with the SAFETY TRANSMITTER can be modeled by considering a series subsystem where failure occurs if there is a failure in either component. For such a system, failure rates are added. Assuming that the SAFETY TRANSMITTER is programmed to drive its output to the specified failure state on detected failures of the thermocouple, the failure rate contribution for the thermocouple in a low stress environment is:

λ_{SU}= (100) * (0.005) = 0.5 FIT

λ_{DD} = (100) * (0.95) = 95 FIT

λ_{DU} = (100) * (0.045) = 4.5 FIT

The total for the temperature sensor assembly with the SAFETY TRANSMITTER (current output) is:

λ_{SU} = 0.5 + 78 = 78.5 FIT

λ_{DD} = 95 + 3400 = 3495 FIT

λ_{DU} = 4.5 + 48 = 53.5 FIT

These numbers could be used in safety instrumented function SIL verification calculations for this set of assumptions. For these circumstances, the Safe Failure Fraction of this temperature sensor assembly is 98.5%.

5.2.2 SAFETY TRANSMITTER with 4-wire RTD

The failure mode distribution for an RTD also depends on the application with key variables being stress level, RTD wire length and RTD type (2/3 wire or 4 wire). The key stress variables are high vibration and frequent temperature cycling as these are known to cause cracks in the substrate leading to broken lead connection welds. Typical failure rate distributions are shown in Table 16. The SAFETY TRANSMITTER will detect open circuit and short circuit RTD failures and drive the analog output to the alarm state on detected failures of the RTD.

Table 16 Failure mode distribution for 4-wire RTD, low stress environment

RTD Failure Modes – Close-coupled device	Percentage
Open Circuit	83%
Short Circuit	5%
Drift (Temperature measurement in error) (50% Safe; 50% Dangerous)	12%

A complete temperature sensor assembly consisting of SAFETY TRANSMITTER and a closely coupled, cushioned 4-wire RTD supplied with the SAFETY TRANSMITTER can be modeled by considering a series subsystem where failure occurs if either component fails. For such a system, failure rates are added. Assuming that the SAFETY TRANSMITTER is programmed to drive its output to the alarm state on detected failures of the RTD, the failure rate contribution for a close-coupled 4-wire RTD in a low stress environment is:

λ_{SU} = (50) * (0.06) = 3 FIT

λ_{DD} = (50) * (0.83 + 0.05) = 44 FIT

λ_{DU} = (50) * (0.06) = 3 FIT

The total for the temperature sensor assembly with the SAFETY TRANSMITTER (current output) is:

λ_{SU} = 3 + 78 = 81 FIT

λ_{DD} = 44 + 3400 = 3444 FIT

λ_{DU} = 3 + 48 = 51 FIT

These numbers could be used in safety instrumented function SIL verification calculations for this set of assumptions. The Safe Failure Fraction for this temperature element, given the assumptions, is 98.7%.

5.3 PFD_{avg} calculation SAFETY TRANSMITTER

Using the failure rate data displayed in section 4.4, and the failure rate data for the associated element devices, an average the Probability of Failure on Demand (PFD_{avg}) calculation can be performed for the element.

Probability of Failure on Demand (PFD_{avg}) calculation uses several parameters, many of which are determined by the particular application and the operational policies of each site. Some parameters are product specific and the responsibility of the manufacturer. Those manufacturer specific parameters are given in this third party report.

Probability of Failure on Demand (PFD_{avg}) calculation is the responsibility of the owner/operator of a process and is often delegated to the SIF designer. Product manufacturers can only provide a PFD_{avg} by making many assumptions about the application and operational policies of a site. Therefore, use of these numbers requires complete knowledge of the assumptions and a match with the actual application and site.

Probability of Failure on Demand (PFD_{avg}) calculation is best accomplished with *exida's* exSILentia tool. See Appendix D for a complete description of how to determine the Safety Integrity Level for an element. The mission time used for the calculation depends on the PFD_{avg} target and the useful life of the product. The failure rates and the proof test coverage for the element are required to perform the PFD_{avg} calculation. The proof test coverages for the suggested proof test are listed in Table 19 Proof Test Coverage – SAFETY TRANSMITTER.

5.4 *exida* Route 2_H Criteria

IEC 61508, ed2, 2010 describes the Route 2_H alternative to Route 1_H architectural constraints. The standard states:

> "based on data collected in accordance with published standards (e.g., IEC 60300-3-2: or ISO 14224); and, be evaluated according to
>
> - the amount of field feedback; and
> - the exercise of **expert judgment**; and when needed
> - the undertake of specific tests,
>
> in order to estimate the average and the uncertainty level (e.g., the 90% confidence interval or the probability distribution) of each reliability parameter (e.g., failure rate) used in the calculations."

exida has interpreted this to mean not just a simple 90% confidence level in the uncertainty analysis, but a high confidence level in the entire data collection process. As IEC 61508, ed2, 2010 does not give detailed criteria for Route 2_H, *exida* has established the following:

1. field unit operational hours of 100,000,000 per each component; and
2. a device and all of its components have been installed in the field for one year or more; and
3. operational hours are counted only when the data collection process has been audited for correctness and completeness; and
4. failure definitions, especially "random" vs. "systematic" [N9] are checked by *exida*; and
5. every component used in an FMEDA meets the above criteria.

This set of requirements is chosen to assure high integrity failure data suitable for safety integrity verification. [N12]

6 Terms and Definitions

Automatic Diagnostics	Tests performed online internally by the device or, if specified, externally by another device without manual intervention.
exida criteria	A conservative approach to arriving at failure rates suitable for use in hardware evaluations utilizing the 2_H Route in IEC 61508-2.
Fault tolerance	Ability of a functional unit to continue to perform a required function in the presence of faults or errors (IEC 61508-4, 3.6.3).
FIT	Failure in Time ($1x10^{-9}$ failures per hour)
FMEDA	Failure Mode Effect and Diagnostic Analysis
HFT	Hardware Fault Tolerance
PFD_{avg}	Average Probability of Failure on Demand
SFF	Safe Failure Fraction, summarizes the fraction of failures which lead to a safe state plus the fraction of failures which will be detected by automatic diagnostic measures and lead to a defined safety action.
SIF	Safety Instrumented Function
SIL	Safety Integrity Level
SIS	Safety Instrumented System – Implementation of one or more Safety Instrumented Functions. A SIS is composed of any combination of sensor(s), logic solver(s), and final element(s).
Type A element	"Non-Complex" element (using discrete components); for details see 7.4.4.1.2 of IEC 61508-2
Type B element	"Complex" element (using complex components such as micro controllers or programmable logic); for details see 7.4.4.1.3 of IEC 61508-2

7 Status of the Document

7.1 Liability

exida prepares FMEDA reports based on methods advocated in International standards. Failure rates are obtained from a collection of industrial databases. *exida* accepts no liability whatsoever for the use of these numbers or for the correctness of the standards on which the general calculation methods are based.

Due to future potential changes in the standards, product design changes, best available information and best practices, the current FMEDA results presented in this report may not be fully consistent with results that would be presented for the identical model number product at some future time. As a leader in the functional safety market place, *exida* is actively involved in evolving best practices prior to official release of updated standards so that our reports effectively anticipate any known changes. In addition, most changes are anticipated to be incremental in nature and results reported within the previous three-year period should be sufficient for current usage without significant question.

Most products also tend to undergo incremental changes over time. If an *exida* FMEDA has not been updated within the last three years, contact the product vendor to verify the current validity of the results.

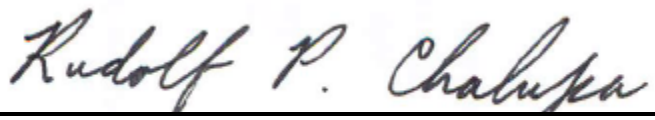
7.2 Releases

Version History:	V3, R3: Added sensor sections, 2017-04-20
	V3, R2: Consolidated IEC 61508 table, corrected formatting, 2017-04-18
	V3, R1: Added DC Relay output options, 2017-04-10
	V2, R2: corrected typos; updated fault injection test data; 2014-04-11
	V2, R1: Updated analysis per current hardware, added status output and unsupervised current output analyses, 2014-04-07
	V1, R2: Updated product name; 2013-10-18
	V1, R1: Released to United Electric Controls; 2013-07-24
	V0, R1: Draft; 2013-07-16
Author(s):	Rudolf Chalupa
Review:	V3, R3: John Yozallinas (exida); 2017-04-20
	V2, R1: John Yozallinas (exida); 2014-04-04
	V0, R1: Chris O'Brien (exida); 2013-07-19
Release Status:	Released to United Electric Controls

7.3 Future enhancements

At request of client.

7.4 Release signatures



Rudolf P. Chalupa, CFSE, Senior Safety Engineer

Chris O'Brien, CFSE, Partner

John Yozallinas, CFSE, Safety Engineer

Appendix A Lifetime of Critical Components

According to section 7.4.9.5 of IEC 61508-2, a useful lifetime, based on experience, should be determined and used to replace equipment before the end of useful life.

Although a constant failure rate is assumed by the *exida* FMEDA prediction method (see section 4.2.2) this only applies provided that the useful lifetime[4] of components is not exceeded. Beyond their useful lifetime the result of the probabilistic calculation method is likely optimistic, as the probability of failure significantly increases with time. The useful lifetime is highly dependent on the subsystem itself and its operating conditions.

Table 17 shows which components are contributing to the dangerous undetected failure rate and therefore to the PFD_{avg} calculation and what their estimated useful lifetime is.

Table 17 Useful lifetime of components contributing to dangerous undetected failure rate

Component	Useful Life
Capacitor (electrolytic) - Tantalum electrolytic, solid electrolyte	Approx. 500,000 hours

It is the responsibility of the end user to maintain and operate the SAFETY TRANSMITTER per manufacturer's instructions. Furthermore, regular inspection should show that all components are clean and free from damage.

The limiting factors with regard to the useful lifetime of the system are the Tantalum electrolytic capacitors. Therefore the useful lifetime is predicted to be 50 years.

When plant experience indicates a shorter useful lifetime than indicated in this appendix, the number based on plant experience should be used.

[4] Useful lifetime is a reliability engineering term that describes the operational time interval where the failure rate of a device is relatively constant. It is not a term which covers product obsolescence, warranty, or other commercial issues.

Appendix B Proof Tests to Reveal Dangerous Undetected Faults

According to section 7.4.5.2 f) of IEC 61508-2 proof tests shall be undertaken to reveal dangerous faults which are undetected by automatic diagnostic tests. This means that it is necessary to specify how dangerous undetected faults which have been noted during the Failure Modes, Effects, and Diagnostic Analysis can be detected during proof testing.

B.1 Suggested Proof Test

The suggested proof test for the SAFETY TRANSMITTER is described in Table 18. Refer to the table in B.2 for the Proof Test Coverages

The suggested proof test consists of a setting the output to the min and max, and a calibration check, see Table 18.

Table 18 Suggested Proof Test – Transmitter

Step	Action
1.	Bypass the safety function and take appropriate action to avoid a false trip.
2.	Inspect the transmitter for any leaks, visible damage or contamination.
3.	Perform a two-point calibration[5] of the transmitter over the full working range.
4.	Remove the bypass and otherwise restore normal operation.

B.2 Proof Test Coverage

The Proof Test Coverage for the various product configurations is given in Table 19.

[5] If the two-point calibration is performed with electrical instrumentation, this proof test will not detect any failures of the sensor

Table 19 Proof Test Coverage – SAFETY TRANSMITTER

Device	Proof Test Coverage
Pressure Current IAW	40%
Temperature Current IAW	35%
Pressure Current no IAW	47%
Temperature Current no IAW	40%
Pressure AC Relay IAW	71%
Temperature AC Relay IAW	71%
Pressure DC Relay IAW	71%
Temperature DC Relay IAW	71%
Pressure Status IAW	49%
Temperature Status IAW	49%

Appendix C *exida* Environmental Profiles

Table 20 *exida* Environmental Profiles

***exida* Profile**	**1**	**2**	**3**	**4**	**5**	**6**
Description (Electrical)	Cabinet mounted/ Climate Controlled	Low Power Field Mounted no self-heating	General Field Mounted self-heating	Subsea	Offshore	N/A
Description (Mechanical)	Cabinet mounted/ Climate Controlled	General Field Mounted	General Field Mounted	Subsea	Offshore	Process Wetted
IEC 60654-1 Profile	B2	C3 also applicable for D1	C3 also applicable for D1	N/A	C3 also applicable for D1	N/A
Average Ambient Temperature	30 C	25 C	25 C	5 C	25 C	25 C
Average Internal Temperature	60 C	30 C	45 C	5 C	45 C	Process Fluid Temp.
Daily Temperature Excursion (pk-pk)	5 C	25 C	25 C	0 C	25 C	N/A
Seasonal Temperature Excursion (winter average vs. summer average)	5 C	40 C	40 C	2 C	40 C	N/A
Exposed to Elements / Weather Conditions	No	Yes	Yes	Yes	Yes	Yes
Humidity[6]	0-95% Non-Condensing	0-100% Condensing	0-100% Condensing	0-100% Condensing	0-100% Condensing	N/A
Shock[7]	10 g	15 g	15 g	15 g	15 g	N/A
Vibration[8]	2 g	3 g	3 g	3 g	3 g	N/A
Chemical Corrosion[9]	G2	G3	G3	G3	G3	Compatible Material
Surge[10]						
Line-Line	0.5 kV	0.5 kV	0.5 kV	0.5 kV	0.5 kV	N/A
Line-Ground	1 kV	1 kV	1 kV	1 kV	1 kV	
EMI Susceptibility[11]						
80 MHz to 1.4 GHz	10 V/m	10 V/m	10 V/m	10 V/m	10 V/m	N/A
1.4 GHz to 2.0 GHz	3 V/m	3 V/m	3 V/m	3 V/m	3 V/m	
2.0Ghz to 2.7 GHz	1 V/m	1 V/m	1 V/m	1 V/m	1 V/m	
ESD (Air)[12]	6 kV	6 kV	6 kV	6 kV	6 kV	N/A

[6] Humidity rating per IEC 60068-2-3
[7] Shock rating per IEC 60068-2-27
[8] Vibration rating per IEC 60068-2-6
[9] Chemical Corrosion rating per ISA 71.04
[10] Surge rating per IEC 61000-4-5
[11] EMI Susceptibility rating per IEC 61000-4-3
[12] ESD (Air) rating per IEC 61000-4-2

Appendix D Determining Safety Integrity Level

The information in this appendix is intended to provide the method of determining the Safety Integrity Level (SIL) of a Safety Instrumented Function (SIF). **The numbers used in the examples are not for the product described in this report.**

Three things must be checked when verifying that a given Safety Instrumented Function (SIF) design meets a Safety Integrity Level (SIL) [N4] and [N7].

These are:

A. Systematic Capability or Prior Use Justification for each device meets the SIL level of the SIF;

B. Architecture Constraints (minimum redundancy requirements) are met; and

C. a PFD_{avg} calculation result is within the range of numbers given for the SIL level.

A. Systematic Capability (SC) is defined in IEC61508:2010. The SC rating is a measure of design quality based upon the methods and techniques used to design and development a product. All devices in a SIF must have a SC rating equal or greater than the SIL level of the SIF. For example, a SIF is designed to meet SIL 3 with three pressure transmitters in a 2oo3 voting scheme. The transmitters have an SC2 rating. The design does not meet SIL 3. Alternatively, IEC 61511 allows the end user to perform a "Prior Use" justification. The end user evaluates the equipment to a given SIL level, documents the evaluation and takes responsibility for the justification.

B. Architecture constraints require certain minimum levels of redundancy. Different tables show different levels of redundancy for each SIL level. A table is chosen and redundancy is incorporated into the design [N8].

C. Probability of Failure on Demand (PFD_{avg}) calculation uses several parameters, many of which are determined by the particular application and the operational policies of each site. Some parameters are product specific and the responsibility of the manufacturer. Those manufacturer specific parameters are given in this third party report.

A Probability of Failure on Demand (PFD_{avg}) calculation must be done based on a number of variables including:

1. Failure rates of each product in the design including failure modes and any diagnostic coverage from automatic diagnostics (an attribute of the product given by this FMEDA report);
2. Redundancy of devices including common cause failures (an attribute of the SIF design);
3. Proof Test Intervals (assignable by end user practices);
4. Mean Time to Restore (an attribute of end user practices);
5. Proof Test Effectiveness; (an attribute of the proof test method used by the end user with an example given by this report);
6. Mission Time (an attribute of end user practices);
7. Proof Testing with process online or shutdown (an attribute of end user practices);
8. Proof Test Duration (an attribute of end user practices); and
9. Operational/Maintenance Capability (an attribute of end user practices).

The product manufacturer is responsible for the first variable. Most manufacturers use the *exida* FMEDA technique which is based on over 250 billion hours of field failure data in the process industries to predict these failure rates as seen in this report. A system designer chooses the second variable. All other variables are the responsibility of the end user site. The exSILentia® SILVer™ software considers all these variables and provides an effective means to calculate PFD_{avg} for any given set of variables.

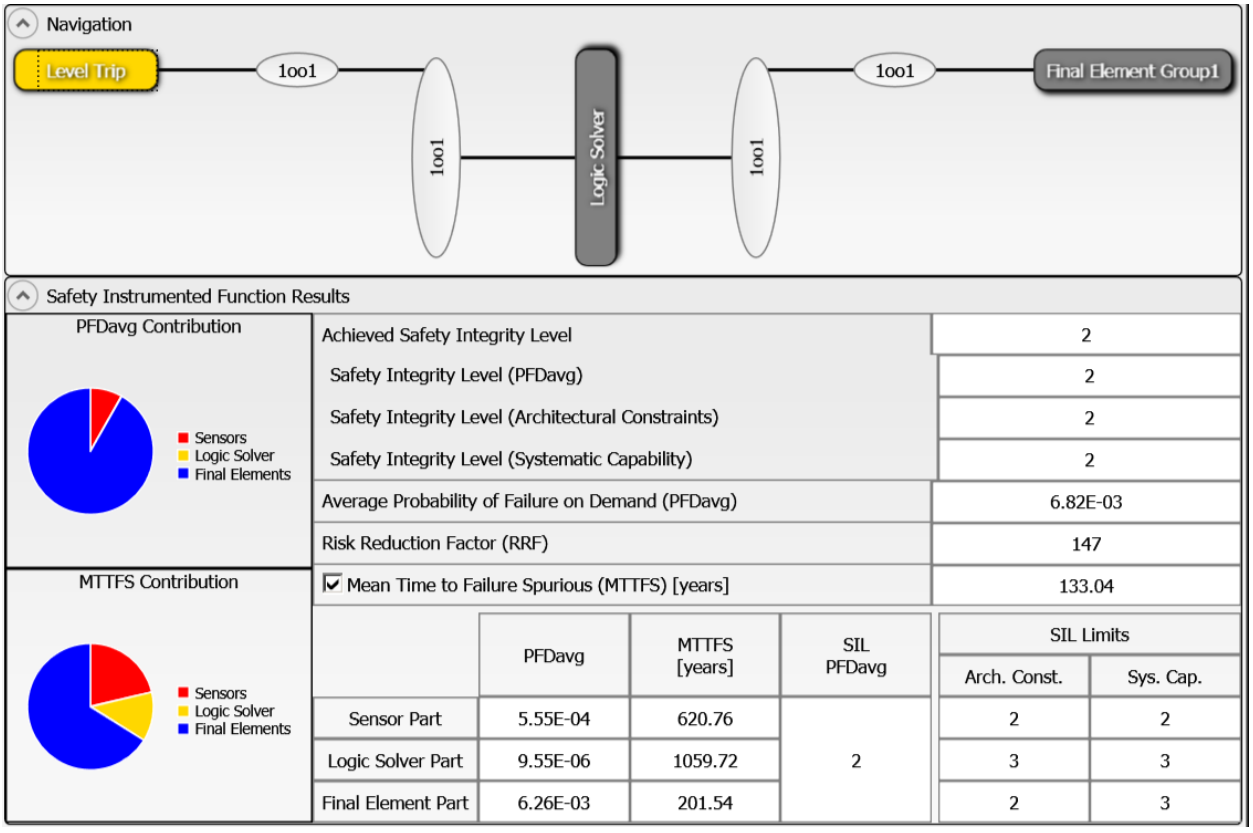
Simplified equations often account for only for first three variables. The equations published in IEC 61508-6, Annex B.3.2 [N1] cover only the first four variables. IEC61508-6 is only an informative portion of the standard and as such gives only concepts, examples and guidance based on the idealistic assumptions stated. These assumptions often result in optimistic PFD_{avg} calculations and have indicated SIL levels higher than reality. Therefore, idealistic equations should not be used for actual SIF design verification.

All the variables listed above are important. As an example consider a high level protection SIF. The proposed design has a single SIL 3 certified level transmitter, a SIL 3 certified safety logic solver, and a single remote actuated valve consisting of a certified solenoid valve, certified scotch yoke actuator and a certified ball valve. Note that the numbers chosen are only an example and not the product described in this report.

Using exSILentia with the following variables selected to represent results from simplified equations:

- Mission Time = 5 years
- Proof Test Interval = 1 year for the sensor and final element, 5 years for the logic solver
- Proof Test Coverage = 100% (ideal and unrealistic but commonly assumed)
- Proof Test done with process offline

This results in a PFD_{avg} of 6.82E-03 which meets SIL 2 with a risk reduction factor of 147. The subsystem PFD_{avg} contributions are Sensor PFD_{avg} = 5.55E-04, Logic Solver PFD_{avg} = 9.55E-06, and Final Element PFD_{avg} = 6.26E-03. See Figure 2.



Achieved Safety Integrity Level	2
Safety Integrity Level (PFDavg)	2
Safety Integrity Level (Architectural Constraints)	2
Safety Integrity Level (Systematic Capability)	2
Average Probability of Failure on Demand (PFDavg)	6.82E-03
Risk Reduction Factor (RRF)	147
☑ Mean Time to Failure Spurious (MTTFS) [years]	133.04

	PFDavg	MTTFS [years]	SIL PFDavg	SIL Limits	
				Arch. Const.	Sys. Cap.
Sensor Part	5.55E-04	620.76	2	2	2
Logic Solver Part	9.55E-06	1059.72		3	3
Final Element Part	6.26E-03	201.54		2	3

Figure 2: exSILentia results for idealistic variables.

If the Proof Test Interval for the sensor and final element is increased in one year increments, the results are shown in Figure 3.

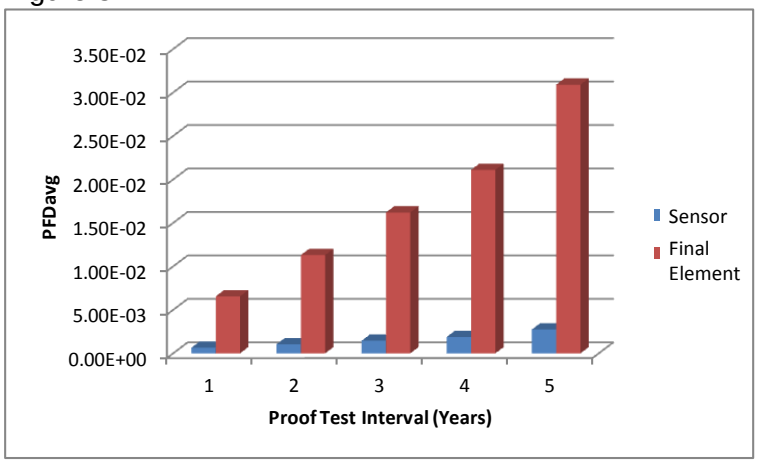


Figure 3 PFD_{avg} versus Proof Test Interval.

If a set of realistic variables for the same SIF are entered into the exSILentia software including:

- Mission Time = 25 years
- Proof Test Interval = 1 year for the sensor and final element, 5 years for the logic solver
- Proof Test Coverage = 90% for the sensor and 70% for the final element
- Proof Test Duration = 2 hours with process online.
- MTTR = 48 hours
- Maintenance Capability = Medium for sensor and final element, Good for logic solver

with all other variables remaining the same, the PFD_{avg} for the SIF equals 5.76E-02 which barely meets SIL 1 with a risk reduction factor 17. The subsystem PFD_{avg} contributions are Sensor PFD_{avg} = 2.77E-03, Logic Solver PFD_{avg} = 1.14E-05, and Final Element PFD_{avg} = 5.49E-02 (Figure 4).

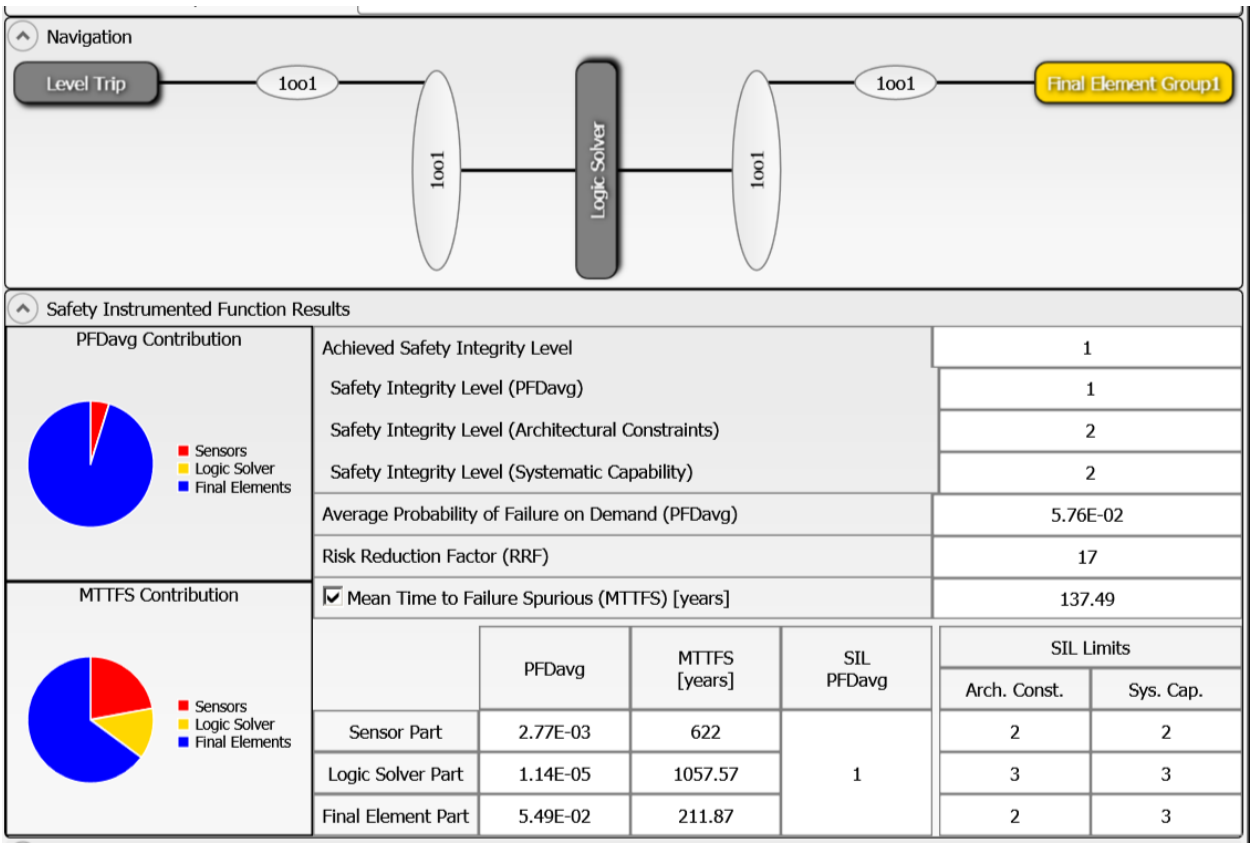


Figure 4: exSILentia results with realistic variables

It is clear that PFD_{avg} results can change an entire SIL level or more when all critical variables are not used.